

Product Name :
VLSI Development Platform for VLSI Design Training Systems Teaching Labs

Product Code :
VLS-TR-02



Description :

VLSI Development Platform - FPGA (XILINX Spartan2) Application Development Board (50K Gates, 1728 logic cells), I/O ports 176, Number of pins 208. The I/O experiments board. Webpack 4.2 Development Software CD.

Naugra Export

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